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			Application Number		
			Filing Date		
			First Named Inventor		
			Group Art Unit		
Examiner Name			Lipeng Cao		
Sheet	1	of	Attorney Docket Number		
			SC12795TS		

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code <sup>2</sup>	Kind (if known)			
J0	AA	5,668,732		Khouja <i>et al.</i>	09/16/1997	
	AB	5,682,320		Khouja <i>et al.</i>	10/28/1997	
	AC	5,696,694		Khouja <i>et al.</i>	12/09/1997	
	AD	5,754,463		Henstrom <i>et al.</i>	05/19/1998	
	AE	6,075,932		Khouja <i>et al.</i>	06/13/2000	
	AF	2003/0097348	A1	Cao	05/22/2003	

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
J0	AG	Joseph <i>et al.</i> , "Run-Time Power Estimation in High Performance Microprocessors," ISLPED'01, August 6-7, 2001, Huntington Beach, California, USA, pp. 135-140.	
J0	AH	Wang <i>et al.</i> , "Efficient Generation of Timing and Power Polynomial Models from Lookup Tables for SoC Designs," 0-7803-5632, IEEE, February 1999, pp. 216-220.	

Examiner Signature	/Juan Ochoa/ (11/27/2006)	Date Considered	
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